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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/692,331	10/23/2003	James L. Jones III	65783-0031	9124

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EXAMINER

CAVALLARI, DANIEL J

ART UNIT PAPER NUMBER

2836

DATE MAILED: 11/07/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/692,331	Applicant(s) JONES ET AL.	
	Examiner Daniel J. Cavallari	Art Unit 2836	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 22 August 2006.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-25 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-16 and 20-25 is/are rejected.
- 7) ☒ Claim(s) 17-19 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 22 August 2006 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO/SB/08) | 5) <input type="checkbox"/> Notice of Informal Patent Application |
| Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

The examiner acknowledges a submission of the amendment filed on 8/22/2006. The amendments to the drawings and to claims 1, 2, 5, 8, 12, 13, 17, 18, 20, 22, & 25 are accepted.

Drawings

The previously made objection to the drawings has been withdrawn in view of the replacement drawings received on 8/22/2006. These new drawings are accepted.

The previously made claim objections to claims 1, 2, 5, 8, 12, 13, 18, & 20 have been withdrawn in view of the amendments to these claims.

Response to Arguments

Applicant's arguments filed 8/22/2006 have been fully considered but they are not persuasive.

The applicant argues that Matsuda fails to teach powering the first and second class devices from a single node, as currently claimed in the amended claims. The examiner respectfully disagrees and points out that Matsuda teaches powering devices (5), which is a second class device, and (6), which is a first class device from a common node, read on by the T2 connection to the power supply, as is the case when there is not a fault condition (See Figure 1 & Column 12, Lines 3-22).

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 1-9, 11-16, & 20-25 are rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuda et al. (US 6,127,741) & Deguchi (US 2004/0041473 A1).

Matsuda et al. (hereinafter referred to as Matsuda) teach

In regard to claims 1, 6, 7, 12, 13, 14, 15, 22, & 25

- A power distribution node of a vehicle read on by components (5 and 6) of Figure 2.
- A primary power feed, read on by line T2 and a secondary power feed read on by line T1 (See Figure 2 & Column 9, Lines 14-34).
- A first switch (321, See Figure 4) of component (6) of Figure 1, for selectively providing power to at least one device (23) of a first class (Class A or B) considered unimportant for a safe operation of the vehicle, read on by a radio (See Column 21, Lines 3-12)
- A second switch (321, See Figure 4) of component (5) of Figure 1, for selectively providing power to at least one device of a second class (Class C) considered

important for the safe operation of the vehicle, read on by headlights (See Column 18, Lines 38-45).

- Under a normal operating state, the first class device (load 23 of component 6 of Figure 1) is selectively powered by the primary power feed (T2) and a second class device (load 23 of component 5 of Figure 1) is selectively powered by the second switch (321 of component 5 of Figure 1).
- Upon disruption of the primary power feed (T2), a second class device (load 23 of component 5 of Figure 1) is selectively powered by said secondary power feed (T1) by the second switch (321 of component 5 of Figure 1) read on by a short circuit occurring in feed line 11 (See Column 14, Line 44-59 & Column 15, line 1-12).
- The first class (6) and second class (5) devices powered by a common node, read on by the T2 connection to the power supply, as is the case when there is not a fault condition (See Figure 1 & Column 12, Lines 3-22).

Matsuda teach switches (321) connecting the individual vehicle loads to the power supply however, is silent as to the particular type of switch used for switch (321) (See Figure 4 & Column 18, Lines 38-45).

Deguchi teaches a vehicle power supply (See Paragraph 2) incorporating individual relays (9, 10) in which to connect loads (11 & 12) both connected to the same supply line of the auxiliary battery (1) under the control of a processor (8) (See Figure 1).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the relays taught by Deguchi in place of the switches (321) taught by Matsuda who is silent in regards to what type of switches are used. The motivation would have been to provide the vehicle loads of Matsuda with a well known and reliable switch capable of providing sufficient current to the vehicle loads.

Matsuda further teaches:

In regard to Claims 2, 3, & 5

- A first switch (read on by breaker 13 [See Figure 2] of component 5 [See Figure 1]) selectively connecting the second relay (switch 321 [See Figure 4] of component 5 [See Figure 2]) to the primary power feed (T2) and a second switch (read on by breaker 14 [See Figure 2] of component 5 [See Figure 1]) selectively connecting the second relay (switch 321 [See Figure 4] of component 5 [See Figure 2]) to the secondary power feed (T1), wherein the first switch is closed and the second switch is open while the system is operating in the normal operating state and the first switch is open and the second switch is closed upon a disruption of the primary power feed (as is the case when the system is normally open at component (4) and a fault occurs on line 11 (See Figure 2 & Column 14, Line 44-59 & Column 15, line 1-12).

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In regard to Claim 4

- Wherein disruption in said primary power feed (T2) occurs when said primary power feed (T2) is placed in a short-circuit (See Column 10, Lines 21-24).

In regard to Claims 8, 20, & 23

- A processor (control unit 30) (See Figure 4) that selectively operates the first and second switches (321) (See Column 18, Lines 58-67).

In regard to Claims 9, 21, & 24

- The processor (30) receives power through a first diode (35) of from a second diode (36) (See Figure 4) in which the diodes are used to isolate the first and second power feeds when the switches are used as an open point (See Column 14, Line 44-59 & Column 15, line 1-12 & Column 18, Lines 28-37).

In regard to Claims 11 & 16

- The first switch (breaker 13) and second switch (breaker 14) comprising of transistors (MOSFET) (See Column 10, Lines 11-15).

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Claim 10 is rejected under 35 U.S.C. 103(a) as being unpatentable over Matsuda et al., Deguchi, & George et al. (US 2004/0066168 A1).

Incorporating all arguments above, Matsuda fail to teach a regulator attached to the power supply to power the processor.

George et al. (hereinafter referred to as George) teaches a controller (44) attached to the power supply through a regulator (58) (See Figure 1 & Paragraph 35).

It would have been obvious to one of ordinary skill in the art at the time the invention was made to incorporate the voltage regulator taught by George into the power system of Matsuda connecting the regulator (58) to the control unit (30) powered by the power supply diodes (35 & 36) (See Figure 4 of Matsuda).

The motivation would have been to provide the processor with a stable voltage supply and allow for the replacement of the processor (30) with different processors with varying voltage requirements.

Allowable Subject Matter

Claims 17, 18 & 19 are objected to as being dependent upon a rejected base claim, but would be allowable, for reasons discussed in the non-final office action, if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

THIS ACTION IS MADE FINAL. Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Daniel J. Cavallari whose telephone number is (571)272-8541. The examiner can normally be reached on Monday-Friday 8:30-5:00.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Brian Sircus can be reached on (571)272-2800 x36. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Daniel Cavallari

October 25, 2006



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